

Fig. 1

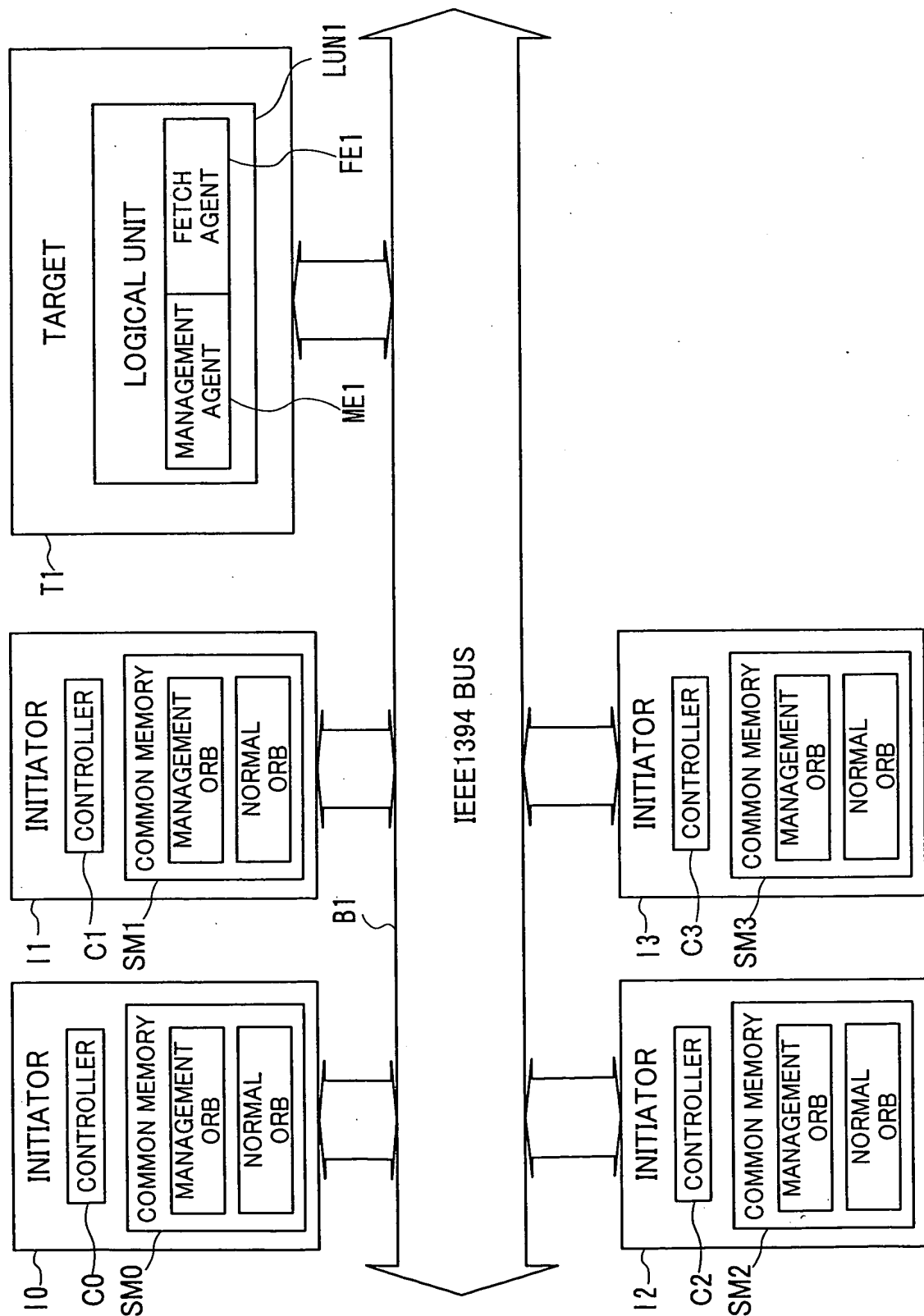


Fig. 2

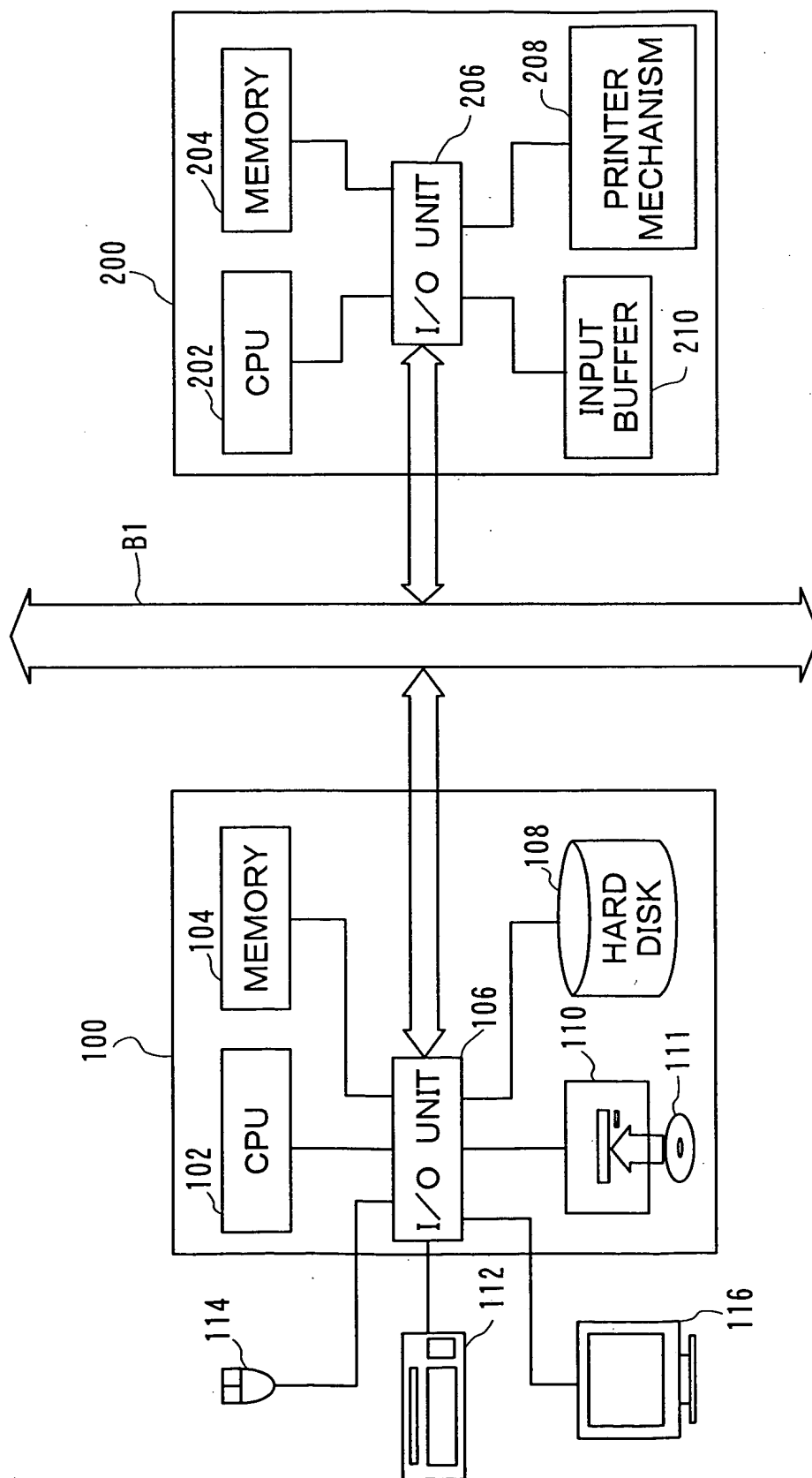
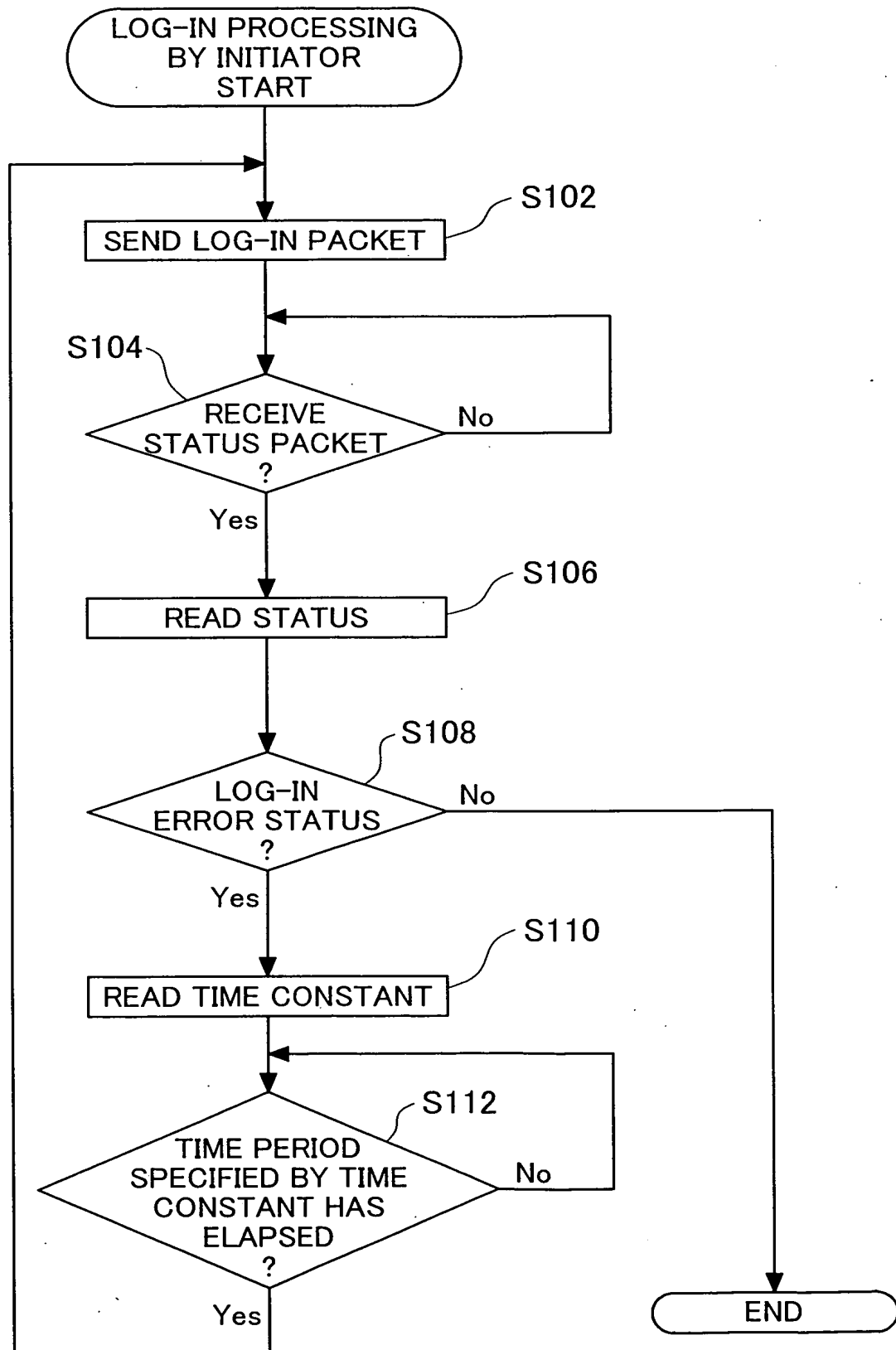
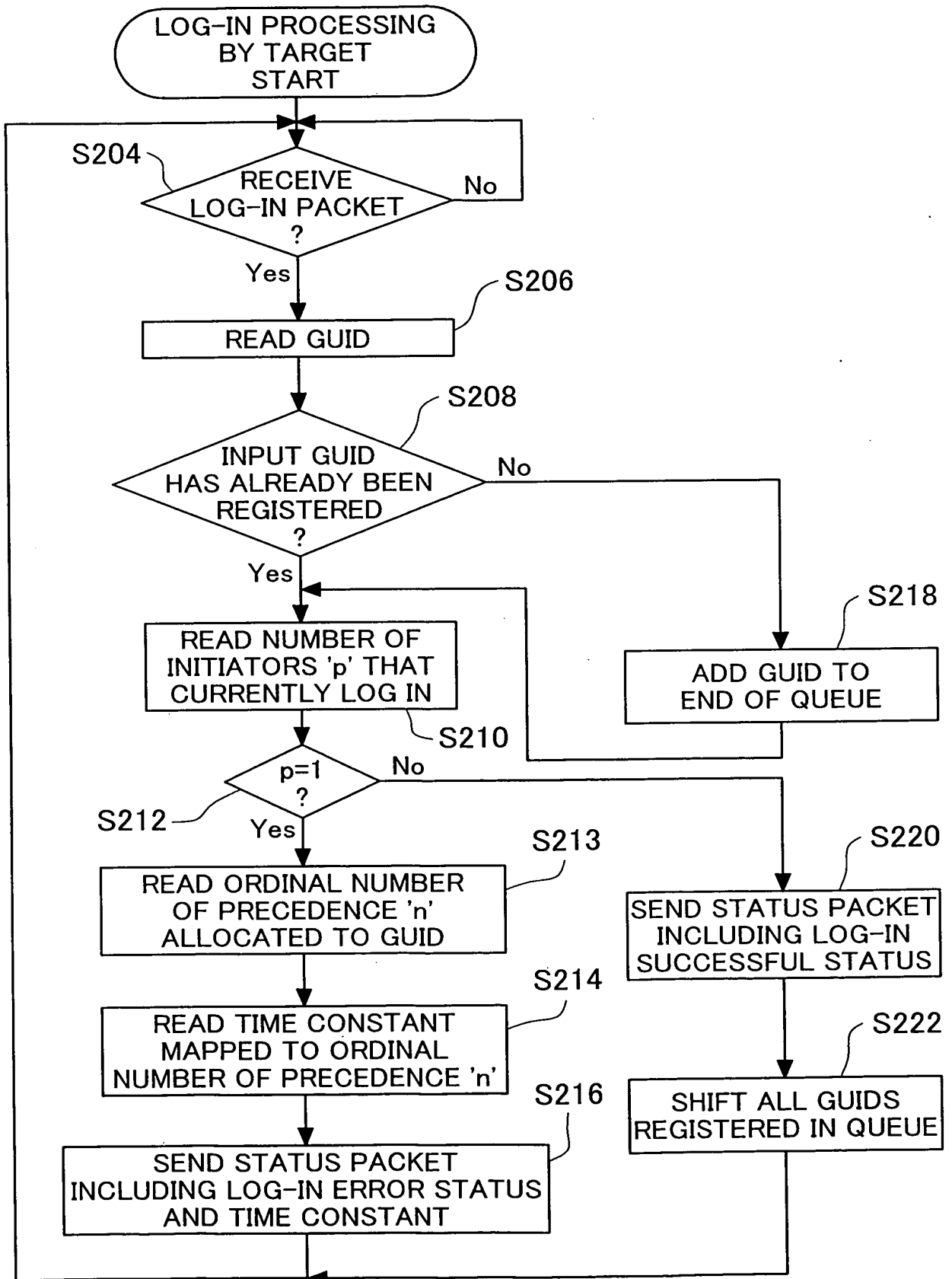


Fig. 3



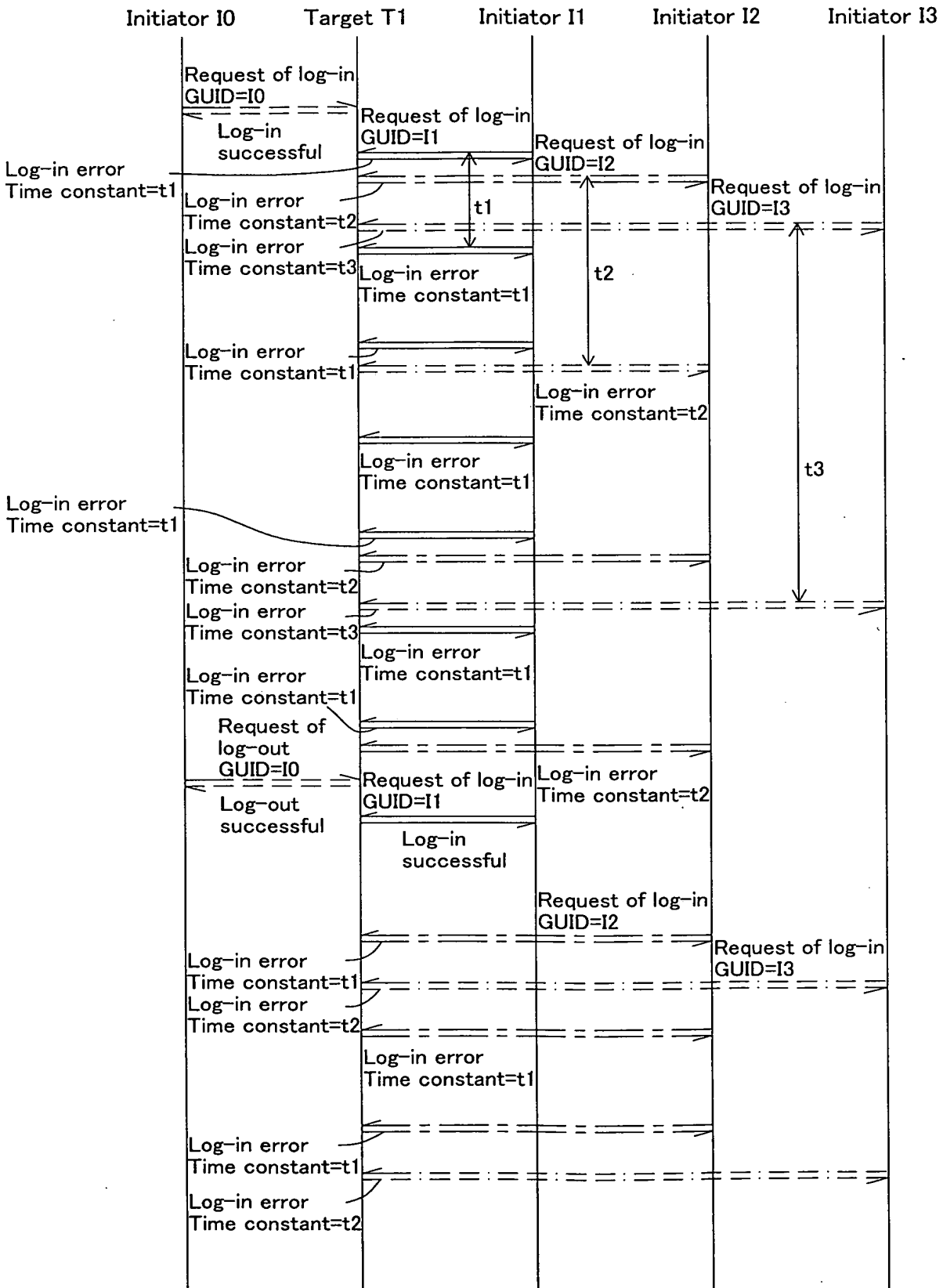
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Fig.4



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Fig. 5



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Fig. 6A

n	GUID
0	
1	
2	
3	
4	
5	
6	
7	

Fig. 6B

n	GUID
0	
1	IO
2	
3	
4	
5	
6	
7	

Fig. 6C

n	GUID
0	IO
1	
2	
3	
4	
5	
6	
7	

Fig. 6D

n	GUID
0	IO
1	I1
2	
3	
4	
5	
6	
7	

Fig. 6E

n	GUID
0	IO
1	I1
2	I2
3	
4	
5	
6	
7	

Fig. 6F

n	GUID
0	IO
1	I1
2	I2
3	I3
4	
5	
6	
7	

Fig. 6G

n	GUID
0	
1	I1
2	I2
3	I3
4	
5	
6	
7	

Fig. 6H

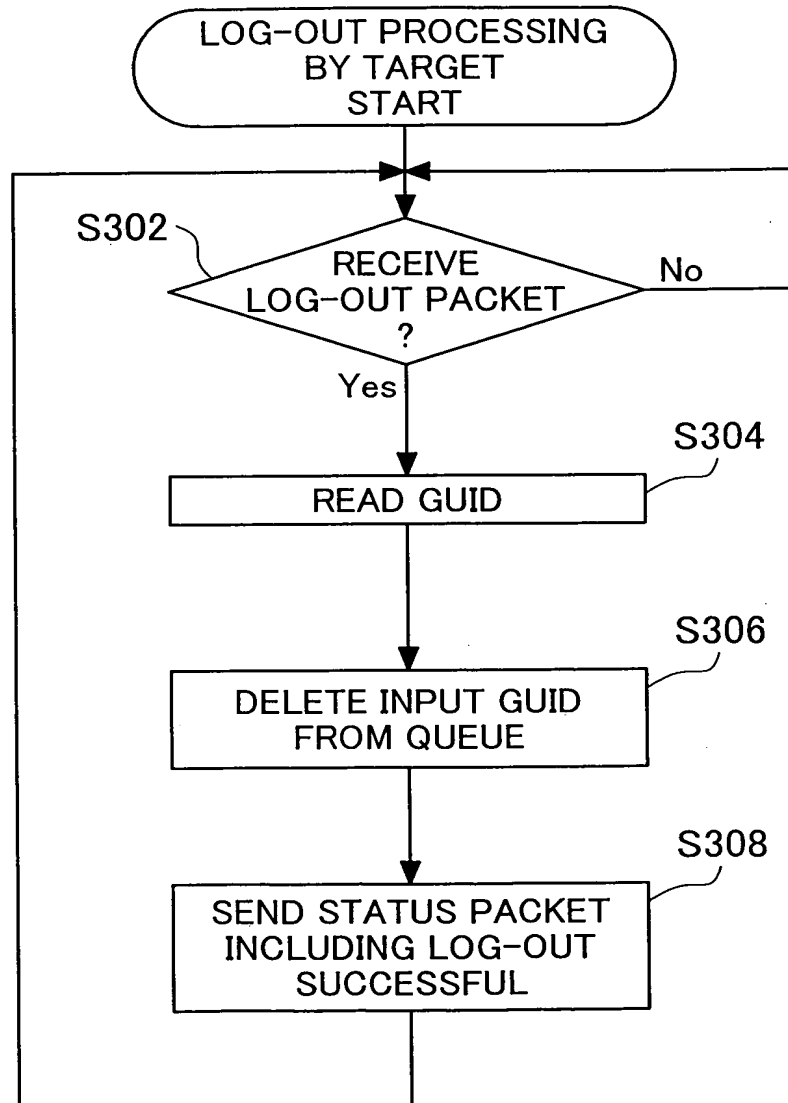
n	GUID
0	I1
1	I2
2	I3
3	
4	
5	
6	
7	

Fig. 7

n	TIME CONSTANT
1	t_1
2	$t_2 (= 2t_1)$
3	$t_3 (= 4t_1)$
4	$t_4 (= 8t_1)$
5	$t_5 (= 16t_1)$
6	$t_6 (= 32t_1)$
7	$t_7 (= 32t_1)$

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Fig. 8



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